

IAS-YNU Seminar 先端科学高等研究院

15th Seminar of Research Unit for Extremely Energy-Efficient Processors 超省エネルギープロセッサ研究ユニット 第15回セミナー

Date: 14:30 – 16:30, March 27 (Monday), 2017

日程: 平成29年3月27日(月) 14:30~16:30

Place: Electrical and Computer Eng. Bldg. 4F, (Seminar room I).
場所: 電子情報工学科棟4階(演習室I)

【Program】

14:30 – 16:30

N. Yoshikawa, Research activities and future plan in IAS

T. Thomas, Analog AQFP feedback circuits

Y. Yamanashi, Analysis current sensitivity of quantum flux parametron

C. Ayala, Recent development of EDA tools and microprocessors for AQFP

Q. Xu, An EDA flow for AQFP VLSI design with customized interface to Cadence tools

R. Sato, Design of an SFQ complex event detector circuit for variable number of symbols

H. Takayama, Simulation of 4-bit random access memory cells composed of quantum flux parametron

Y. Xing, Design of power divider for AQFP logic

A. Sanada, Study on high-speed dividing circuit based on single flux quantum logic

Y. Okuma, Demonstration of AQFP/CMOS hybrid memory